



## PhD Cell

Ref.: Implementation Order No: PhD-04/03-2025/01

Date: Mar 26, 2025

**Subject: Implementation Order for the execution of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at Indian Institute of Technology (BHU), Varanasi**

1. The undersigned is directed to convey the approval of Competent Authority for allotment of 3 Full Time PhD seats for implementation of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at Indian Institute of Technology (BHU), Varanasi for **Academic Year 2025-26**.
2. The estimated budget outlay for 3 Full Time PhD candidates of your institution under Visvesvaraya PhD Scheme is **Rs. 1,08,92,400/- (Rupees One Crore Eight Lakh Ninety Two Thousand Four Hundred only)** with a duration of 60 months or PhD completion of the enrolled candidates, whichever is earlier. Details are in the Annexure (attached).
3. Before proceeding with the enrollment of candidate(s) on the allocated seat(s), the institution needs to accept the 'Terms and Conditions' including 'Standard Operating Procedure' (attached). The acceptance should be duly signed & stamped, by the Head of the Institution of Indian Institute of Technology (BHU), Varanasi. Soft copies of the signed and stamped documents should be uploaded on the PhD Scheme portal and sent to phd-scheme@digitalindia.gov.in latest by the end of Apr 2025. The original documents need to be sent to the undersigned at the following address: -

Sandeep Bansal, In-charge, PhD Cell, Room No. 2084, Electronics Niketan, 6, CGO Complex, Lodhi Road, New Delhi-110003.

4. The enrolment of PhD candidates followed by their verification & other formalities as per the 'T&C' should be completed by Sep 2025 of the Academic Year 2025-26.
5. The candidates enrolled and verified by the institution would be eligible to receive monthly 'Fellowship' & 'Reimbursement of Rent' which will be transferred directly to the bank accounts of the PhD candidates. The institution is required to submit periodic performance/progress reports for each candidate enrolled under the scheme. In case of non-performance of any candidate, change in fellowship due to absence etc., change in the amount of RoR due to change in accommodation, or drop-out etc.; the institution is required to, intimate PhD Cell, DIC; regarding the same immediately and obtain the acknowledgment of the receipt of such intimation from PhD Cell. In case no such intimation is received by PhD Cell, DIC will continue to transfer the fellowship & RoR directly to the bank accounts of PhD candidates and recovery of the excess amount would be the responsibility of the institution. For more details, please refer to the Guidelines & SoP as attached.
6. The PhD Cell team would be interacting with the Nodal Officer of your institution for routine matters, however, in some cases the intervention of the Head of the Institution would be required for which the PhD Cell team will keep updating the institution through different channels e.g. phd-scheme@digitalindia.gov.in or telephonically.

Sandeep Bansal  
(In-charge, PhD Cell, DIC)

To,  
Prof. Amit Patra  
Director  
Indian Institute of Technology (BHU), Varanasi

Copy to:

1. Dr. Amritanshu Pandey, Nodal Officer, Indian Institute of Technology (BHU), Varanasi
2. Shri Nand Kumarum, IAS, MD&CEO, Digital India Corporation
3. Shri K. P. Sivasdas, Director (Finance), Digital India Corporation

Electronics Niketan Annexe, 6, CGO Complex Lodhi Road, New Delhi - 110003 (India)

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Website: www.dic.gov.in

# Digital India Corporation

Annexure to the Implementation order no.: PhD-04/03-2025/01 dated Mar 26, 2025

Sr. No.	Name of the Scheme/ Project	'Visvesvaraya PhD Scheme for Electronics and IT: Phase II at Indian Institute of Technology (BHU), Varanasi
1	Objective of the Scheme/Project	Successful completion of PhD, by the candidates to be enrolled against the PhD seats, as allotted in this Implementation Order, following the guidelines of the scheme and periodic directions from PhD Cell, Digital India Corporation, MeitY.
2	Area of Coverage	ESDM and IT/ ITES.
3	Name of Implementing Agency	<b>Indian Institute of Technology (BHU), Varanasi</b>
4	Location/ Address	Indian Institute of Technology (BHU), Varanasi, Hindu University Campus, Varanasi, Uttar Pradesh 221005
5	No. of Full Time PhD Candidates	3
6	Total Scheme/ Project Duration	60 months or PhD completion of the enrolled Candidate(s), whichever is earlier

## Total Scheme/ Project Outlay:

Budget Head	Amount in Rs.					
	1st Yr	2nd Yr	3rd Yr	4th Yr	5th Yr	Total
Full Time fellowship @ Rs. 38750/- per month in 1 <sup>st</sup> & 2 <sup>nd</sup> Years and Rs. 43750/ per month in 3 <sup>rd</sup> , 4 <sup>th</sup> and 5 <sup>th</sup> years of PhD	1395000	1395000	1575000	1575000	1575000	7515000
Reimbursement of Rent (RoR) (Class Y @ 16%)	223200	223200	252000	252000	252000	1202400
Institutional Overhead	75000	75000	75000	75000	75000	375000
Research Contingency Grant	360000	360000	360000	360000	360000	1800000
<b>Total</b>	<b>2053200</b>	<b>2053200</b>	<b>2262000</b>	<b>2262000</b>	<b>2262000</b>	<b>10892400</b>

*Sandeep Kumar*  


Electronics Niketan Annexe, 6, CGO Complex Lodhi Road, New Delhi – 110003 (India)

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